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				Application Number	TBD
				Filing Date	Herewith
				First Named Inventor	Chee Kiang Yew, et al.
				Group Art Unit	TBD
				Examiner Name	TBD
Sheet	1	of	2	Attorney Docket No.	TI-26239.1

U.S. PATENT DOCUMENTS						
Exam. Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
PC	AA	5,061,509		Nalto, et al.	10/29/1991	
PC	AB	5,353,498		Fillion, et al.	10/11/1994	
PC	AC	5,739,585		Akram, et al.	04/14/1998	
PC	AD	5,776,796		Distefano, et al.	07/07/1998	
PC	AE	5,981,314		Glenn, et al.	11/09/1999	
PC	AF	6,218,202 B1		Yew, et al.	04/17/2001	
PC	AG	6,228,676 B1		Glenn, et al.	05/08/2001	
PC	AH	6,242,283 B1		Lo, et al.	06/05/2001	
	AI					
	AJ					
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FOREIGN PATENT DOCUMENTS								
Exam. Initials*	Cite No. 1	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T°
		Office ³	Number ⁴	Kind Code ² (if known)				
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				Group Art Unit	TBD
				Examiner Name	TBD
Sheet	2	Of	2	Attorney Docket No.	TI-26239.1

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
pc	CA	"Silicon Processing for the VLSI Era", Stanley Wolf, Ph.D., et al., Volume 1: Process Technology, Lattice Press, Sunset Beach, CA, 1988, pp. 547-551	
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Examiner Signature	PHAT X. CAO	Date Considered	2/12/04
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